

Department of Electronics & Communication Engineering
School of Studies Engineering & Technology
Guru Ghasidas Vishwavidyalaya, Bilaspur (C.G.)
(A Central University)

Admission to Ph.D. Programme (2018-19)

VRET Category

VRET TOTAL SEATS: 08 UN-RESERVED (05)

OBC (02)

ST (00)

SC (01)

The following candidates are qualified in the VRET-2018 for admission to Ph.D. Programme in Electronics & Communication Engineering.

S.No.	Roll No.	Name Of The Student	Category	Marks Obtained
1	10051610009	Prashant Upadhyay	GENERAL	51
2	10051610006	Nikhil Kumar Yadav	OBC	48
3	10051610007	Pallavee Jaiswal	OBC	46

VRET Exempted Category

VRET EXEMPTED TOTAL SEATS: 03

UN-RESERVED (03)

The candidates appearing in the VRET Exempted category are instructed to report for Interview along with a power point presentation on research proposal along with all the relevant original documents.

S.No.	Registration No.	Name Of The Student	Category	Remark
1	1819851124358	Sumit Kumar Gupta	GENERAL	Regular Faculty
2	18198223224471	Pragati Patharia	SC	Hardcopy of form not received
3	18198913324695	Chandan Tamrakar	OBC	Hardcopy of form not received
4	18198623324877	Devendra Rani	OBC	UGC/CSIR NET
5	1819921125216	Sourabh Tiwari	GENERAL	Hardcopy of form not received
6	18199523325581	Chetana	OBC	Not eligible due to UG/PG degree not in Engineering
7	1819812126136	Suchita Shrivastava	GENERAL	Hardcopy of form not received
8	18199113326188	Vinay Kumar Gupta	OBC	UGC/CSIR NET

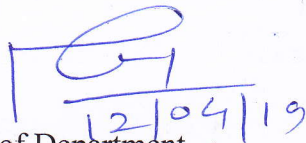
The above candidates are required to attend for personal interview/presentation on the scheduled date and time as detailed below. The candidates under VRET-Exempted category may also be required to give a presentation at the time of interview. The final selection is based on the qualifying marks in VRET, Interview/presentation/viva-voce and interest of student in the area of expertise available with the faculty member of the Department.

At the time of interview, the candidates are required to bring all the relevant original documents (Class X, XII, Degree, PG, GATE Score card, UGC/CSIR NET certificate, Transfer certificate, Migration certificate, Character certificate, latest Caste certificate (SC/ST/OBC Non- creamy layer if any), Fee Payment receipt, Hard copy of the online application form submitted, Two passport size colour photographs, along with one set of self-attested copies) as per the details mentioned in Ph.D. Regulations/VRET 2018-19 Information Bulletin.

Interview Schedule

Date and Time of Registration/Documents verification : 29-04-2019 at 10:00 AM
Date and Time of Interview and Presentation : 29-04-2019 at 11:00 AM onwards
Venue : Electronics & Communication Engineering
Department, New IT Building, GGV, Koni,
Bilaspur, Chhattisgarh 495009

Note: The admission is subject to the minimum eligibility criteria, verification of all the relevant documents and performance in the interview/presentation. Mere attending the interview does not entitle admission to Ph.D. programme in the Department.


Head of Department
Electronics & Communication Engineering
प्रभागध्यक्ष (इले. एवं संचार अभियंत्रिकी)
H.O.D. (Elect. & Comm. Engineering)
प्रौद्योगिकी संस्थान
Institute of Technology
गु. मा. वि., बिलासपुर (छ.ग.)
G. V. Bilaspur (C.G.)
